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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BOARD OF PATENT APPEALS AND INTERFERENCES**

Appellant(s): Kazunori KISHIMOTO  
Serial No.: 10/083,447  
Filed: February 26, 2002  
For: METHOD OF TESTING A SEMICONDUCTOR  
INTEGRATED CIRCUIT AND METHOD AND  
APPARATUS FOR GENERATING TEST PATTERNS  
Examiner: John P. Trimmings  
Group Art Unit: 2133  
Attorney Docket No.: NEKO 19.481 (100806-00091)

12/23/2005 HTECKLU1 00000051 501290 10083447

December 22, 2005

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**PETITION FOR EXTENSION OF TIME**

Sir,

Applicant requests that the time for taking action in this case be extended pursuant to 37 CFR 1.136(a) for:

( ☒ ) one month ( ) three months  
( ) two months ( ) four months

The fee set in 37 CFR 1.17 for a one-month extension of time is \$120.

( ) Fee enclosed. Please charge any additional fee required for this extension of time to Deposit Account No. 50-1290. A duplicate copy of this paper is enclosed.

( ☒ ) Charge fee to Deposit Account No. 50-1290. A duplicate copy of this paper is enclosed.

( ) Applicant is a small entity entitled to pay reduced fees in this application.

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Appln. No.:10/083,447

A verified small entity statement:

( ) has been filed ( ) is enclosed.

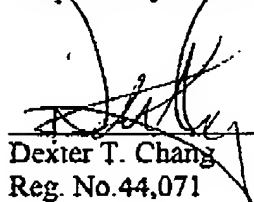
Also enclosed is a:

( ) Election

(X) Brief for Appellants

( ) Response/ Amendment

Respectfully submitted,



Dexter T. Chang  
Reg. No.44,071

**CUSTOMER NUMBER 026304**

KATTEN MUCHIN ROSENMAN  
575 Madison Avenue  
New York, NY 10022-2585  
(212) 940-8800  
Docket No.: NEKO 19.481  
DC: pm

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